

The process of the scratch-induced selective etching in H 2 SO 4 /H 2 O 2 /H 2 O solution. With an AFM, two scanning modes, i.e. line and area scanning, were used for scratching on GaAs surface, and then the scanned surface was etched in the mixed solution. AFM images show the scanned GaAs surface before and after selective etching.

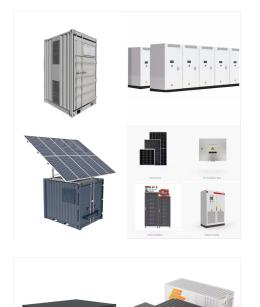


3 Department of Materials Science and Engineering, Massachusetts Institute of Technology, Massachusetts 02139, Selective defect etching was used to estimate the etch pit density J. Faucher, and M. L. Lee, "Defect selective etching of GaAsyP1???y photovoltaic materials," J. Cryst. Growth 404, 140???145 (2014).



where y???etching rate, X 1, X 2, X 3 ???the concentration of etchant components in the solution ((NH 4) 2 Cr 2 O 7, HBr and EG, accordingly), k 1 ???k 15 ???parameters of the model (Cornell 1986).. The dependence of the etch rate on the modifier concentration was considered for the range 0???95 vol.% CH 2 (OH)CH 2 (OH).. Etch rate was estimated by the electronic ???





A defect selective wet chemical etching technique that allows accurate determination of etch pit density (EPD) in thin Germanium (Ge) layers is described. The effect is achieved by using chromium

It is essential to know the etching mechanisms to understand and predict material-selective etching. The chapter also discusses the resulting macroscopic (that is, at single crystal faces) as well



The notes include data on materials, etch rates and specific etch conditions when possible. material selective etchants, defect revealing etchants, profile etching; Ref. (Notten, P.H.L., 1993) III???V semiconductor etchant review: gives pre-1962 ???









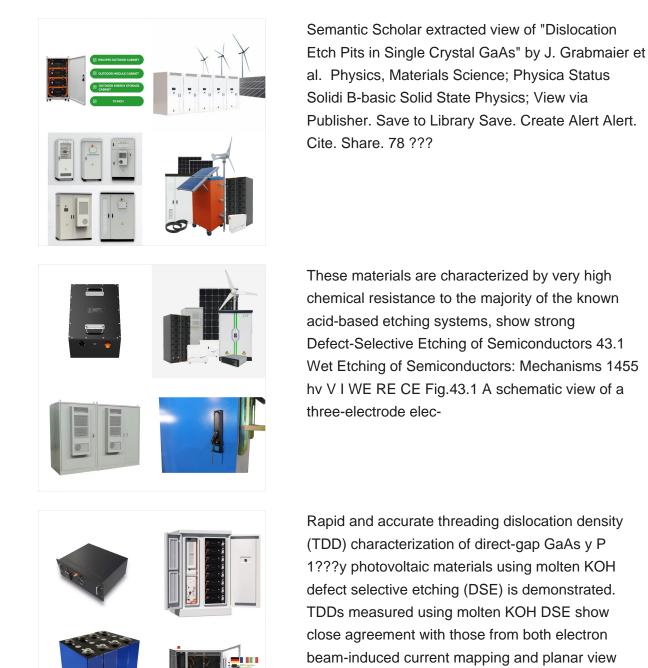
Rapid and accurate threading dislocation density (TDD) characterization of direct-gap GaAs y P 1???y photovoltaic materials using molten KOH defect selective etching (DSE) is demonstrated. TDDs measured using molten KOH DSE show close agreement with those from both electron beam-induced current mapping and planar view transmission electron ???



Controlling the electrical properties of SiC requires knowledge of the nature and properties of extended defects. We have employed orthodox defect-selective etching and photo-etching methods to reveal typical and new structural defects in commercial SiC wafers. For photo-etching, the etch rate increases as the free carrier concentration decreases. The etch rate can ???

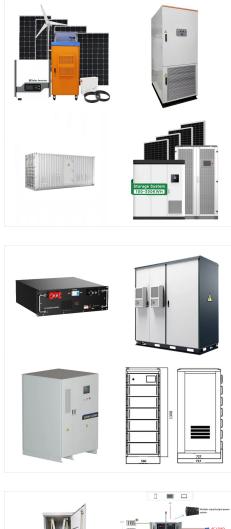
It is found that the etch pits revealed by molten KOH etching on the (001) GaAs surface have definitely different shapes depending on the Burgers vectors of the dislocations. The round hexagonal etch pit corresponds to a dislocation with Burgers vector of 1/2 a???101??? inclined to the (001) plane, while the small round square etch pit corresponds to a dislocation with Burgers ???

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transmission electron microscopy, provided ???





Owing to its unique optical properties, light-trapping structure plays a more and more important role in photovoltaic devices [].At present, researchers have developed all kinds of nanostructures as light-trapping structures to increase light absorption in photovoltaics, while most of them were performed on Si substrate [2,3,4,5,6].III???V compound semiconductor ???

SiC; the etch depth of the perfect SiC matrix was 100 nm. These defects are not observed on polished surfaces before etching. Some hexagonal defects, such as the one indicated by the arrow, show complex morphology. A magnied view of the boundary of this non-homogeneously etched area is shown in Fig. 1b. The etch depth prole along the y???y" line



GaAsyP1-y anion-sublattice compositionally graded buffers and device structures were grown directly on Si(100) substrates by way of a high-quality GaP integration layer, yielding GaAsP ???





Physical Review B???Condensed Matter and Materials Physics 78 (3), 035201, 2008. 31: S Tomasulo, KN Yaung, ML Lee. IEEE Journal of Photovoltaics 2 (1), 56-61, 2012. 29: 2012: Simultaneous large mode index and high quality factor in infrared hyperbolic metamaterials Defect selective etching of GaAsyP1??? y photovoltaic materials. KN



In the present chapter we first briefly consider mechanisms for the etching of semiconductors (Sect. 43.1) and relate these principles to methods for controlling surface morphology and revealing defects (Sect. 43.2).Section 43.3 describes in some detail defect-sensitive etching methods. Results are presented for the classical (orthodox) method used for revealing ???



DOI: 10.1016/J.JCRYSGRO.2016.08.015 Corpus ID: 99891786; Threading dislocation density characterization in III???V photovoltaic materials by electron channeling contrast imaging





Rapid and accurate threading dislocation density (TDD) characterization of direct-gap GaAs y P 1-y photovoltaic materials using molten KOH defect selective etching (DSE) is demonstrated.



A method is reported in order to determine an upper bound for the Threading Dislocation (TD) density in experimental GaAs solar cells grown lattice-mismatched on Si. The method is based on the modeling of the devices'' External Quantum Efficiency (EQE), using the classic drift-diffusion model, or Hovel model. The model is fitted to experimental EQE ???



The etching rate on a {001} plane of GaAs in molten KOH as the etchant is examined in order to reveal etch pits corresponding to dislocations for thin GaAs layers. The mean etching rates were 0.083 um/min. and 0.98 um/min. at 300?C and 350?C, respectively. It was found that the etching thickness of about 0.3 um was sufficient to reveal the dislocation structure.

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Solar Energy Materials and Solar Cells. 2019; 33. PDF. Defect selective etching of GaAsyP1???y photovoltaic materials. K. N. Yaung S. Tomasulo J. R. Lang J. Faucher M. Lee. Materials Science, Physics. 2014; 14. Save. 40.8% efficient inverted triple-junction solar cell with two independently metamorphic junctions.

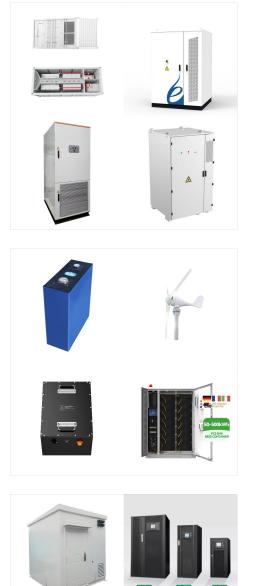


Plan-view transmission electron microscopy (PVTEM), electron beam-induced current (EBIC), cathodoluminescence (CL), and defect selective etching (DSE) are commonly used to obtain TDD, with well-known advantages and disadvantages (Table 1) [10].PVTEM has been a commonly used technique due to its ability to clearly distinguish various types of ???



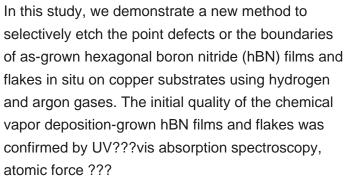
Control of defect processes in photovoltaic materials is essential for realizing high-efficiency solar cells and related optoelectronic devices. Native defects and extrinsic dopants tune the Fermi





Prototype GaAsP solar cell test devices grown on anion-sublattice step-graded GaAs y P 1-y buffers on early-stage GaP/Si substrates show good preliminary performance characteristics ???

Metamorphic growth of InAsSb layers on GaAs substrate can solve this limitation and allow cost-competitiveness compared to the high-cost substrates such as GaSb and InAs [4], [5], [6].Early attempts to do this involved the direct growth of the InAsSb layer on a GaAs substrate, and more recently, the use of a single intermediate InSb layer without any strain ???







Rapid and accurate threading dislocation density (TDD) characterization of direct-gap GaAs y P 1???y photovoltaic materials using molten KOH defect selective etching (DSE) is demonstrated.